

FIG. 1 is a block diagram of a system 1000 for processing an image. The system 1000 includes a processor 1050, a memory 1050a, a RIP 1050b, an image controller 1100, and an imager 1150. The processor 1050 is connected to the memory 1050a and the RIP 1050b. The image controller 1100 is connected to the processor 1050 and the imager 1150. The imager 1150 is connected to the image controller 1100. The system 1000 is used for processing an image.

1000

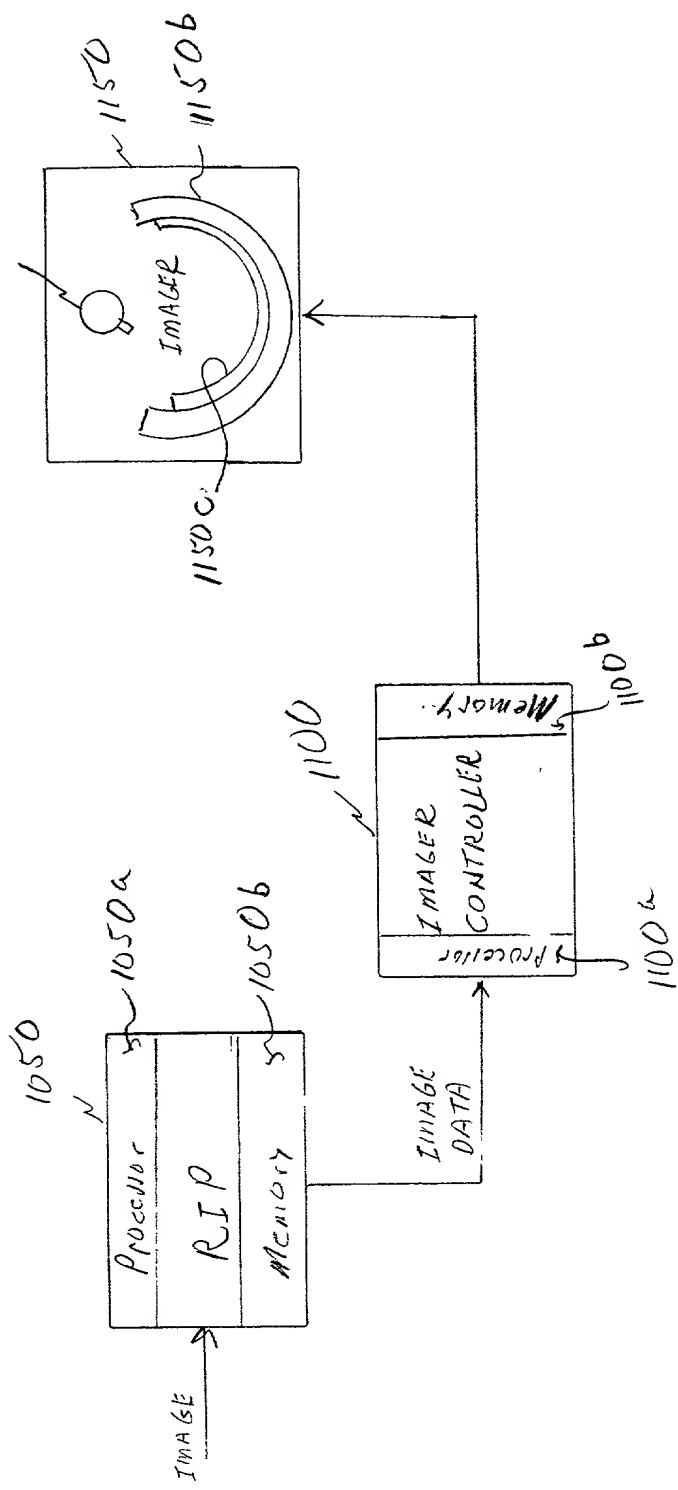


FIGURE 1

2000

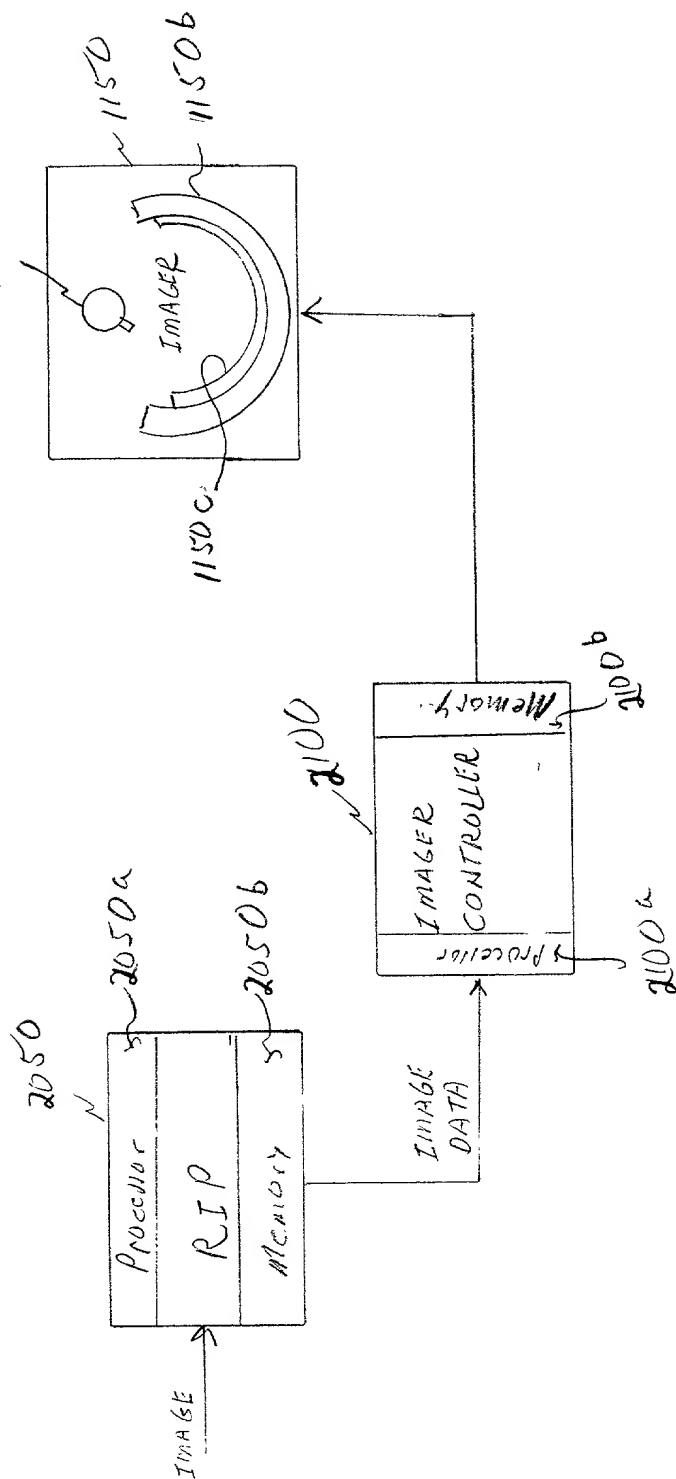


FIGURE 2

Code Dictionary	
Code Number	Representing
100	0 (white)
101	1 (black)
102	Reset
103	END
104	New Patterns
105	ab
106	bc
107	cd
108	01
109	1c
110	1071 (c01)

Prefined Codes  
1330

New Codes  
1340

FIGURE 3

21300